

What is claimed is:

1. A microcomputer, comprising:

a first timer having first resolution that can be used for controlling cooling equipment; and

a second timer that has second resolution higher than said first resolution and that can be used for adjusting the brightness of a display device,

wherein said first timer and said second timer are formed on a semiconductor integrated circuit.

2. The microcomputer according to claim 1,

wherein said first timer outputs a signal of a first frequency, and

wherein said second timer outputs a signal of a second frequency higher than said first frequency.

3. The microcomputer according to claim 1, wherein said second timer can be further used for controlling the charge and discharge of a battery.

4. The microcomputer according to claim 3, wherein said first frequency and said second frequency frequency-divides and generates a standard clock signal respectively.

5. The microcomputer according to claim 1, wherein said

second timer comprises a pulse width conversion circuit.

6. The microcomputer according to claim 2,
wherein said first timer can output a plurality of signals
of a first frequency, and

wherein said second timer can output a plurality of
signals that is the signal of the second frequency higher than
said first frequency and differs in a pulse width.